

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	285	(splitter\$1 or de\$1multiplexer\$1) with (chassis or box or enclosure\$1 or casing\$1 or hous\$3) and (PCB or circuit adj board\$1) and \$4communication\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/14 15:54
L2	3050	(splitter\$1 or de\$1multiplexer\$1) and (chassis or box or enclosure\$1 or casing\$1 or hous\$3) and (PCB or circuit adj board\$1) and \$4communication\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/14 15:16
L3	146	L2 and edge adj connector\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/14 14:56
L4	20	L3 and edge adj connector\$1 with (splitter\$1 or de\$1multiplexer\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/14 14:59
L5	18	L3 and card adj edge with (splitter\$1 or de\$1multiplexer\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/14 14:59
L6	29	edge adj connector\$1 with (splitter\$1 or de\$1multiplexer\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/14 15:06
L7	95	L3 and edge adj connector\$1 with (PCB or circuit adj board\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/14 15:45

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L8	21	L7 and (splitter\$1 or de\$1multiplexer\$1) with edge with connector\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/14 15:44
L9	2	"5850230".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/14 15:43
L10	1	L9 and edge adj connector\$1 with (PCB or circuit adj board\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/14 15:55
L13	1	L10 and (splitter\$1 or de\$1multiplexer\$1) and edge with connector\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/14 15:53
L14	0	L13 and (splitter\$1 or de\$1multiplexer\$1) same edge with connector\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/14 15:46
L16	160	379/413.04.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/14 15:54
L17	6	L16 and (splitter\$1 or de\$1multiplexer\$1) with (chassis or box or enclosure\$1 or casing\$1 or hous\$3) and (PCB or circuit adj board\$1) and \$4communication\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/14 15:56

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L18	2	L17 and edge adj connector\$1 with (PCB or circuit adj board\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/14 15:56
L19	2	L17 and edge adj connector\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/14 15:57
L20	4918	379/325-332,399.01-413.04.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/14 15:56
L21	23	L20 and (splitter\$1 or de\$1multiplexer\$1) with (chassis or box or enclosure\$1 or casing\$1 or hous\$3) and (PCB or circuit adj board\$1) and \$4communication\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/14 15:57
L22	10	L21 and edge adj connector\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/14 15:57